Docket No.: 4481-044



In re Application of

David Martin Gee

Serial No. To Be Assigned

Filed: Concurrently Herewith

Group Art Unit: Unknown

Examiner: Unknown

For: FAULT TOLERANT ELECTRICAL CIRCUIT AND METHOD

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D. C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Respectfully submitted,

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ATTY. DOCKET NO. SERIAL NO. INFORMATION DISCLOSURE 4481-044 To Be Assigned CITATION IN AN **APPLICATION** (PTO-1449) APPLICANT **David Martin Gee** FILING DATE **GROUP Concurrently Herewith** Unknown **U.S. PATENT DOCUMENTS EXAMINER'S FILING INITIALS** PATENT NO. DATE NAME **CLASS SUBCLASS** DATE 5,618,752 04/08/97 GAUL 5,943,574 08/24/99 TEHRANI et al. FOREIGN PATENT DOCUMENTS **EXAMINER'S** Translation **INITIALS** PATENT NO. DATE COUNTRY CLASS **SUBCLASS** Yes No 0 924 823 A1 EPO 06/23/99 XX 6-69210 03/11/94 **JAPAN** XX OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) (1) European Search Report. (2) English-language Patent Abstract of JP 6-69210. (3) English-language Patent Abstract of JP 2000 311316. (4) English-language Patent Abstract of JP 01 161860. **EXAMINER** DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.